

03-02-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Express Mail No.: EL721030126

RECEIVED

Technology (S)

Applicant:

Eric Lindemann et al.

: February 28, 2001

Serial No.:

09/707,616

Filed: For:

November 7, 2000

6 2001 Technology Center 2600

DIGITAL WIRELESS LOUDSPEAKER SYSTEM

INFORMATION DISCLOSURE STATEMENT

Attached hereto is Form PTO-1449 listing documents believed relevant to the above identified application. It is respectfully requested that these documents be considered by the Examiner and placed of record in this application, and an initialed copy of the form be returned to the undersigned.

A copy of each reference is enclosed.

None of the references, alone or in combination, disclose a digital wireless loudspeaker system, as is taught and claimed in this application.

> Respectfully submitted, Eric Lindemann et al.

By

Jennifer L. Bales

Attorney Reg. No. 38,070 Telephone (303) 664-4734 Facsimile (303) 664-4735

e-mail: bales@mbj-law.com

Enclosures

A02.134



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Case Docket No.: A02. 19 agy Center 2600

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

Eric Lindemann et al.

: February 22, 2001

Serial No.:

09/707,616

: GAU 2747

Filed:

November 7, 2000

For:

DIGITAL WIRELESS LOUDSPEAKER SYSTEM

LETTER TO EXAMINER

Assistant Commissioner for Patents Washington, D.C. 20231

To Whom It May Concern:

The following co-pending patent applications by the same assignee may contain related subject matter:

U.S. Patent Applications:

Serial No.:

09/457,014

Inventor:

John Melanson

Title:

Calibration and Compensation of Delta Sigma ADC's and DAC's by

Correlating Noise Signals

Filing Date:

December 8, 1999

Serial No.:

09/452,904

Inventors:

Eric Lindemann, John Melanson, Jason Carlson, and Jim Kates

Title:

Digital Wireless Loudspeaker System

Filing Date:

December 1, 1999

Serial No.:

09/510,035

Inventor:

John Melanson

Title:

Power Output Stage Compensation for Digital Output Amplifiers

Filing Date:

February 22, 2000

Serial No.:

09/510,034

Inventor:

John Melanson

Title:

Methods and Apparatus for Correction of Higher Order Delta Sigma

Convertors

Filing Date:

February 22, 2000

Serial No.:

09/510,818

Inventor:

John Melanson

Title:

Digital to Analog Converter Using Level and Timing Contol Signals to

Cancel Noise

Filing Date:

February 23, 2000

Serial No.:

09/526,784

Inventor:

John Melanson

Title:

Filing Date:

Technology Center 2600 Power Supply Compensation for Noise Shaped, Digital Amplifiers

MAR 0 1 200

March 16, 2000

Serial No.: Inventor:

09/404.452 John Melanson

Title:

Output Stage Utilizing a Floating Power Supply

Filing Date:

September 22, 1999

Serial No.:

09/592,145 John Melanson

Inventor: Title:

Real Time Correction of Digital PWM Amplifier

Filing Date:

June 12, 2000

Serial No.:

09/480,274 John Melanson

Inventor: Title:

Adaptive Dead Time Control for Switching Circuits

Filing Date:

January 11, 2000

Serial No.:

09/633,505

Inventor:

John Melanson

Title:

Dynamic Element Matching Apparatus and Methods

Filing Date:

August 7, 2000

Serial No.:

09/633,381 John Melanson

Inventor: Title:

Dynamic Element Matching in an ADC

Filing Date:

August 7, 2000

Serial No.:

09/707.616

Inventors:

Eric Lindemann et al.

Title:

Digital Wireless Loudspeaker System

Filing Date:

November 7, 2000

PCT Patent Applications:

Serial Number:

PCT/US97/07420 John Melanson

Inventor: Title:

Delta Sigma PWM DAC to Reduce Switching

Filing Date:

May 2, 1997

Serial Number:

PCT/US98/08899

Inventors:

Eric Lindemann and Thomas Worrall

Title:

Continuous Frequency Dynamic Range Audio Compressor

Filing Date:

May 1, 1998

Serial No.:

PCT/US99/22621 John Melanson

Inventor:

Title:

Correction of Nonlinear Output Distortion in a Delta Sigma DAC

Filing Date:

September 29, 1999

Serial No.:

PCT/US99/29164

Inventor:

John Melanson

Title:

Calibration and Compensation of Delta Sigma ADC's and DAC's by

Correlating Noise Signals

Filing Date:

December 9, 1999

Serial No.:

PCT/US99/28686

Inventors:

PCT/US99/28080
Eric Lindemann, John Melanson, Jason Carlson, and Jim Kates

Title:

Digital Wireless Loudspeaker System

Filing Date:

December 3, 1999

Serial No.:

PCT/US00/04409

Inventors:

John Melanson

Title:

Methods and Apparatus for Correction of Higher Order Delta Sigma

Convertors

Filing Date:

February 22, 2000

Serial No.: Inventor:

PCT/US00/40010 John Melanson

Title:

Power Supply Compensation for Noise Shaped, Digital Amplifiers

Filing Date:

March 16, 2000

Serial No.:

PCT/US00/25911

Inventor:

John Melanson

Title:

Output Stage Utilizing a Floating Power Supply

Filing Date:

September 21, 2000

Serial No.:

PCT/US00/40975 John Melanson

Inventor: Title:

Adaptive Dead Time Control for Switching Circuits

Filing Date:

September 23, 2000

Applicant respectfully requests that the application be allowed to issue as soon as possible. The Examiner is welcome to contact the undersigned if this will clarify any issues or otherwise advance the prosecution of this application.

Respectfully submitted, Eric Lindemann et al.

By

Jennifer L. Bales

Registration No. 38,070 Telephone (303) 664-4734 Facsimile (303) 664-4735



Sheet 1 of 1.

Form PTO-1449								DEMARKE	Docket Number (Optional) A02.134	Applica 09/70	Application Number 09/707,616			
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